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Robin E. Barnum	(Depositor's name)
	(Signature)
January 11, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/955,993	09/20/2001	Yutaka Toyonoh	TI-33251 (032350.B343)	9873

TITLE OF INVENTION: BOOTH ENCODING CIRCUIT FOR A MULTIPLIER OF A MULTIPLY-ACCUMULATE MODULE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	03/30/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
DO, CHAT C	2124	708-628000

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- 1 Robert D. Marshall, Jr.
2 W. James Brady, III
3 Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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Texas Instruments Incorporated

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Dallas, TX USA

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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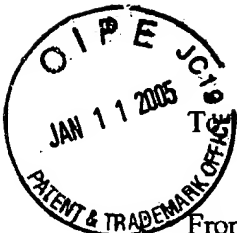
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-33251

Yutaka Toyonoh, et al.

Art Unit: 2124

Serial No.: 09/955,993

Examiner: Chat C. Do

Filed: September 20, 2001

Conf. No.: 9873

For: Booth Encoding Circuit for a Multiplier of a Multiply-Accumulate Module

CERTIFICATION OF FACSIMILE TRANSMISSION

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NAME OF INVENTOR(S): Yutaka Toyonoh, et al.		RECEIPT DATE & SERIAL NO.: Serial No.: 09/955,993 Filing Date: September 20, 2001
TITLE OF INVENTION: Booth Encoding Circuit for a Multiplier of a Multiply-Accumulate Module		
TI FILE NO.: TI-33251	DEPOSIT ACCT. NO.: 20-0668	
FAXED: 1/11/05 DUE: 3/30/05 ATTY/SEC'Y: RDM/reb		

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